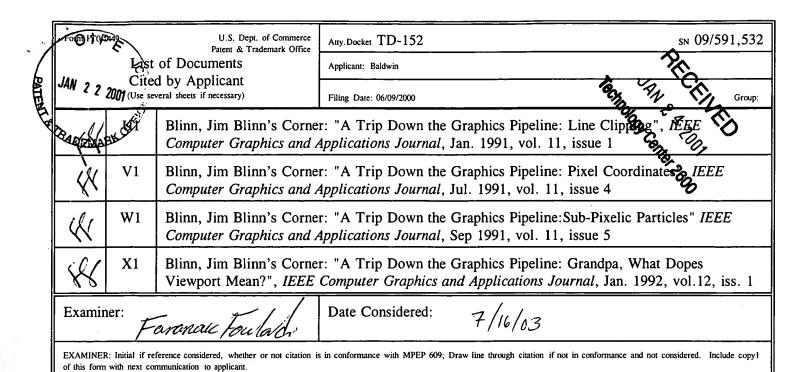
U.S. Dept. of Commerce Atty.Docket TD-152 sn 09/591,532 Patent & Trademark Office List of Documents Applicant: Baldwin JAN 2 2 2001Cited by Applicant (Use several sheets if necessary) Filing Date: 06/09/2000 RADE MAEN U.S. PATENT DOCUMENTS Class Ex'rs Name Document Date Number 8/20/96 FF A<sub>1</sub> 5,548,709 Hannah et al. **B**1 5,611,064 3/11/97 Maund et al. C1 5,706,481 1/6/98 Hannah et al. 10/27/98 Wilde **D**1 5,828,382 **E**1 5,831,640 11/3/98 Wang et al. F1 5,842,015 11/24/98 Cunniff et al. 3/9/99 Griffin et al. G1 5,880,737 5,886,706 3/23/99 Alcorn et al. H1 J1 5,999,189 12/7/99 Kajiya et al. 6,002,407 12/14/99 Fadden **K**1 L1 6,002,410 12/14/99 Battle M1 6,011,565 1/4/00 Kuo et al. OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) Cox et al., "Multi-Level Texture Caching for 3D Graphics Hardware," Proceedings of the 25th N<sub>1</sub> International Symposium on Computer Architechture, 1998 Foley et al., Computer Graphics: Principles and Practice (2.ed. 1990, corr. 1995), pages 741-744 01 Hakura and Gupta, "The Design and Analysis of a Cache Architecture for Texture Mapping," **P**1 Proceedings of the 24th International Symposium on Computer Architechture, 1997 Paul S. Heckbert, "Fundamentals of Texture Mapping and Image Warping," Thesis submitted to Q1 Dept. of EE and Computer Science, University of California, Berkeley, 6/17/94 Heckbert, "Survey of Computer Graphics," IEEE Computer Graphics, November 1986, pp.56 R1 **S1** Igehy et al., "Prefetching in a Texture Cache Architecture", IEEE T1 Blinn, Jim Blinn's Corner: "Dirty Pixels", IEEE Computer Graphics and Applications Journal, Jan. 1989, vol. 9, issue 4



IDS (paper #4) is a reference which was cited in IDS for the application as reference "OI", but was not included at the time of Filing and therefore was sent in separately.